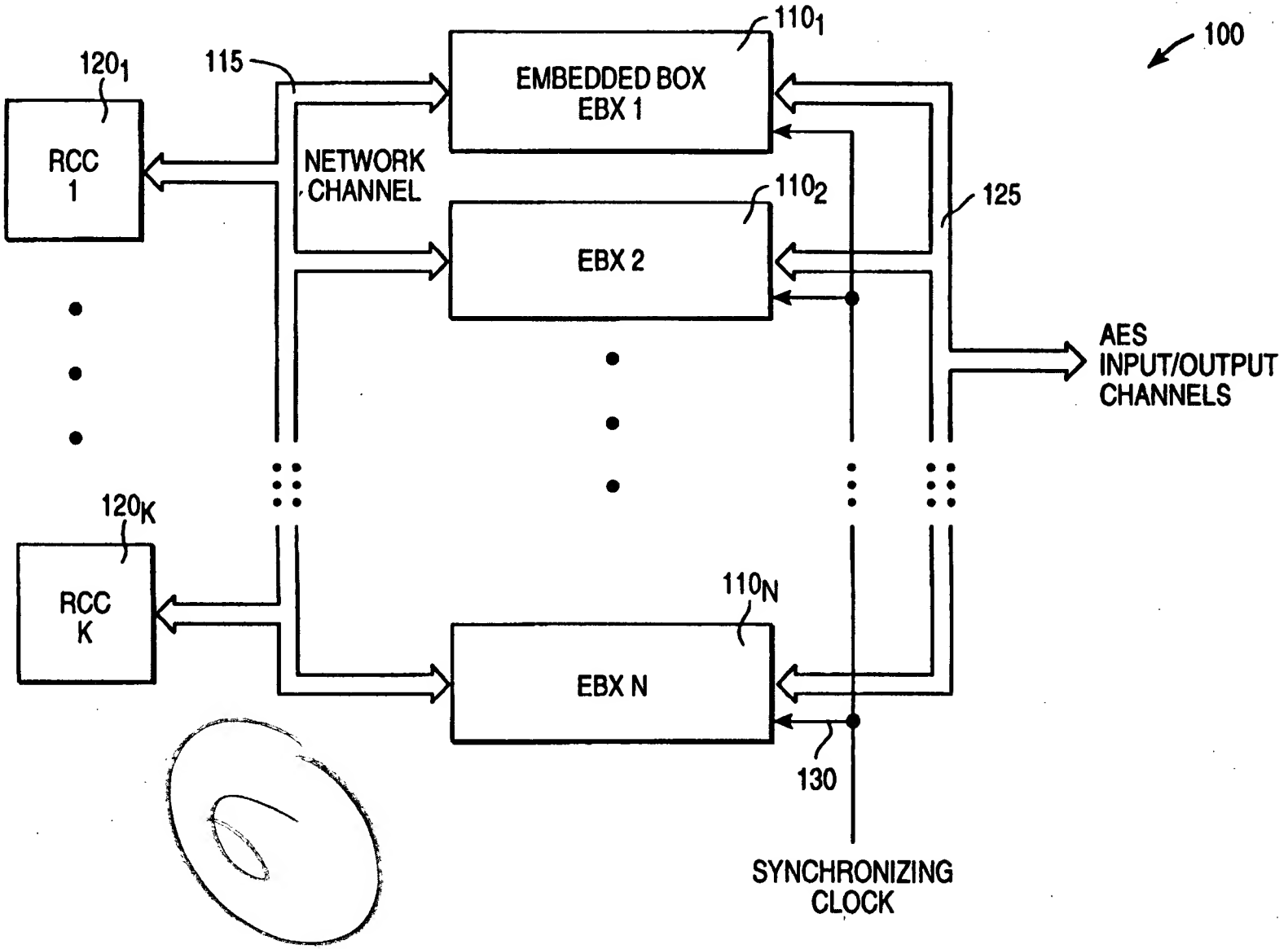
100
110_1
EMBEDDED BOX
EBX 1
120_1
115
RCC
1
NETWORK
CHANNEL
110_2
EBX 2
125
AES
INPUT/OUTPUT
CHANNELS
120_K
RCC
K
110_N
EBX N
130
SYNCHRONIZING
CLOCK

FIG_1

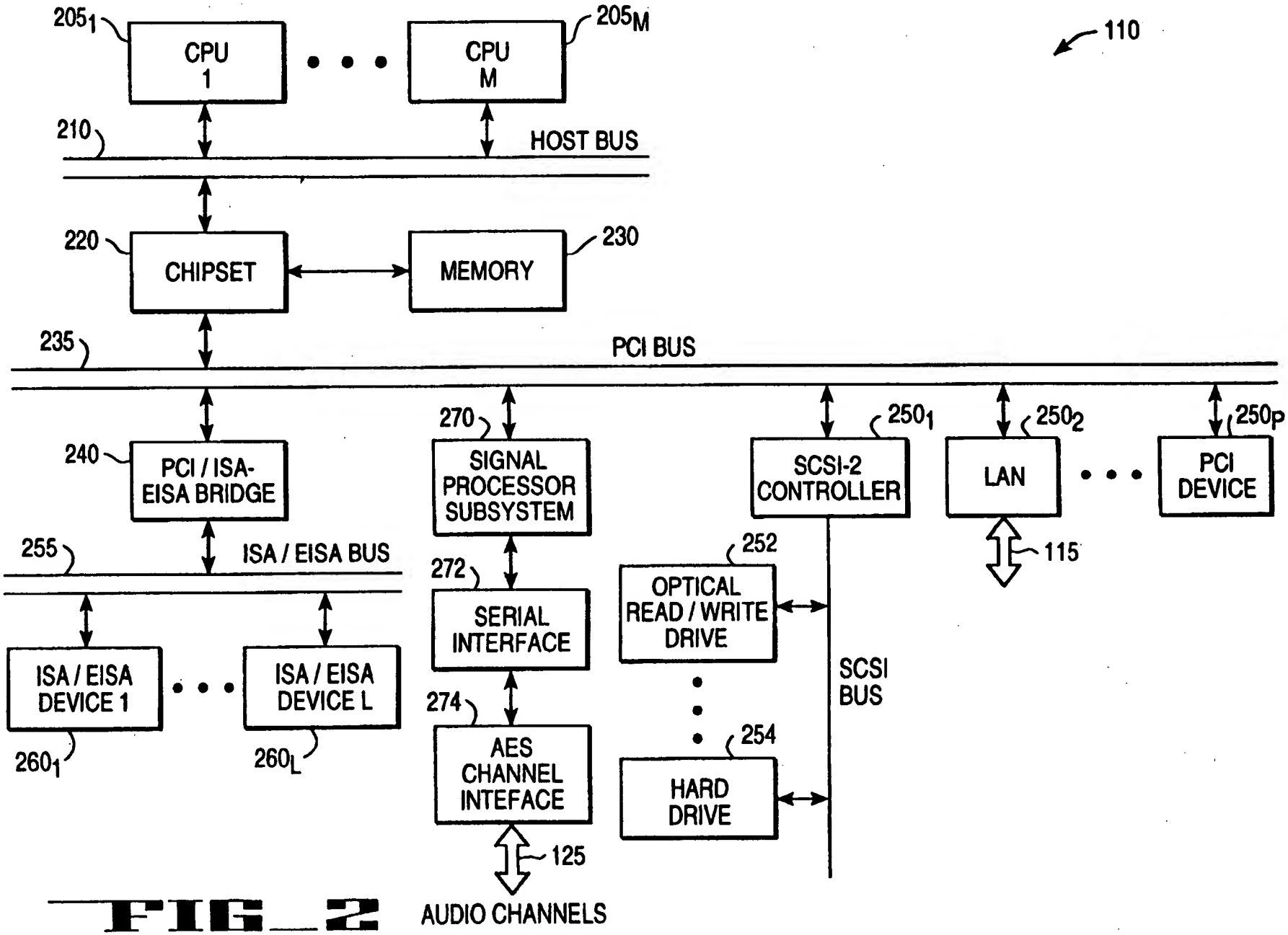
205₁
CPU 1
CPU M
205M
110
210
HOST BUS
220
CHIPSET
MEMORY
230
235
PCI BUS
240
PCI / ISA-EISA BRIDGE
270
SIGNAL PROCESSOR SUBSYSTEM
250₁
SCSI-2 CONTROLLER
250₂
LAN
250p
PCI DEVICE
255
ISA / EISA BUS
272
SERIAL INTERFACE
252
OPTICAL READ / WRITE DRIVE
115
ISA / EISA DEVICE 1
ISA / EISA DEVICE L
260₁
260L
274
AES CHANNEL INTEFACE
254
HARD DRIVE
SCSI BUS
125
AUDIO CHANNELS

FIG_2

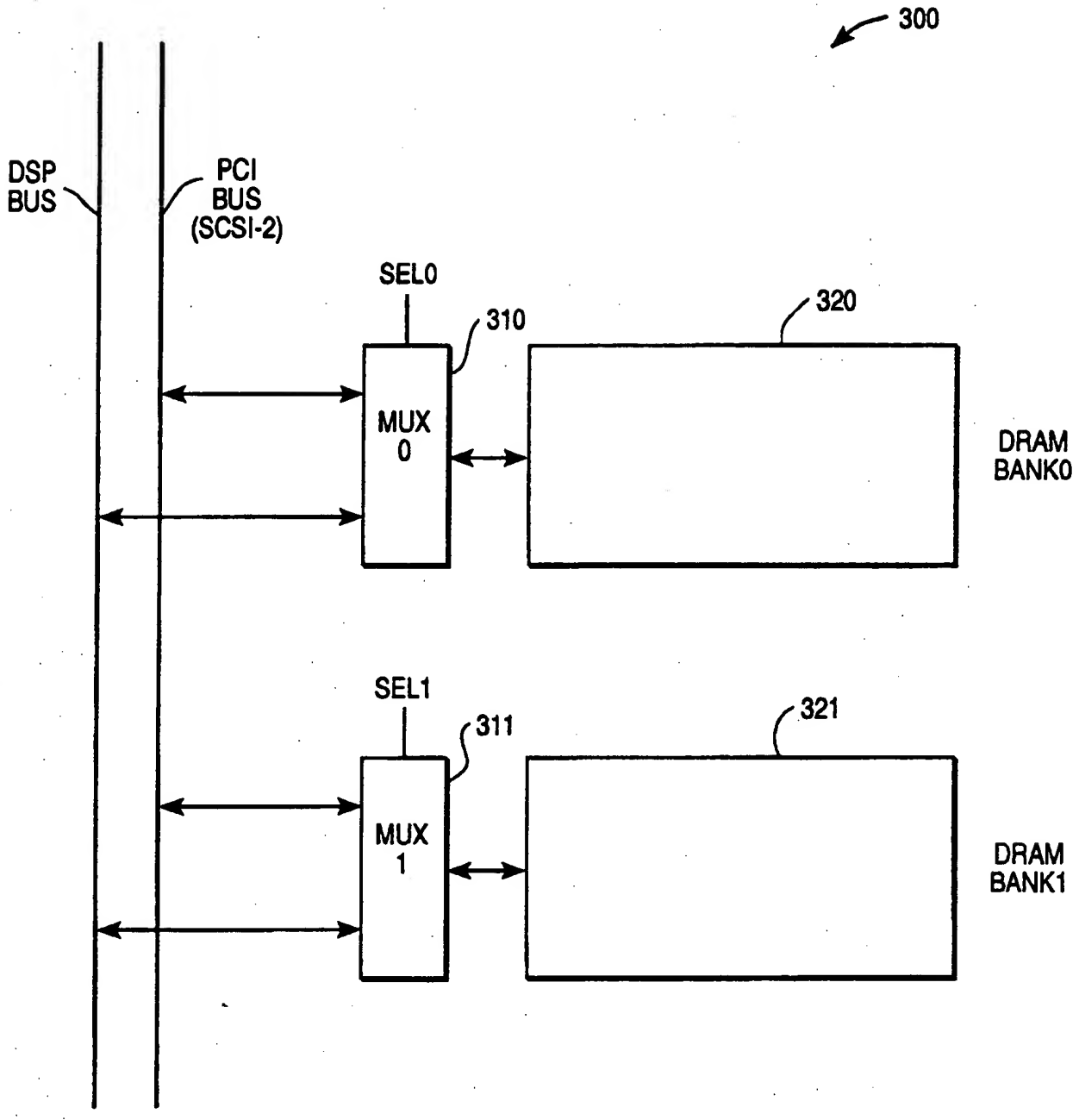
300
DSP
BUS
PCI
BUS
(SCSI-2)
SEL0
310
320
MUX
0
DRAM
BANK0
SEL1
311
321
MUX
1
DRAM
BANK1

FIG_3

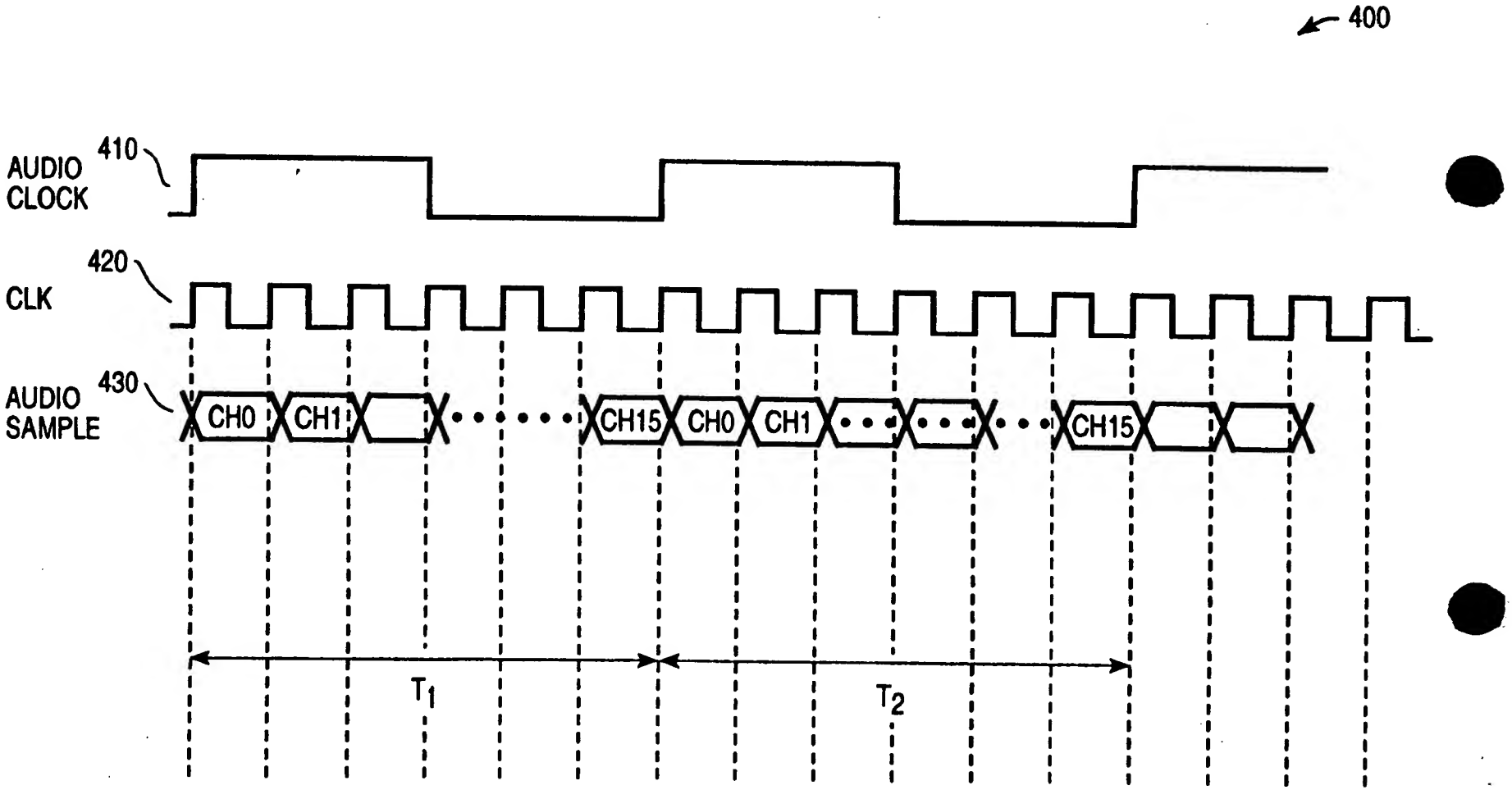
400
410
AUDIO CLOCK
420
CLK
430
AUDIO SAMPLE
CH0
CH1
CH15
CH0
CH1
CH15
T1
T2

FIG_4

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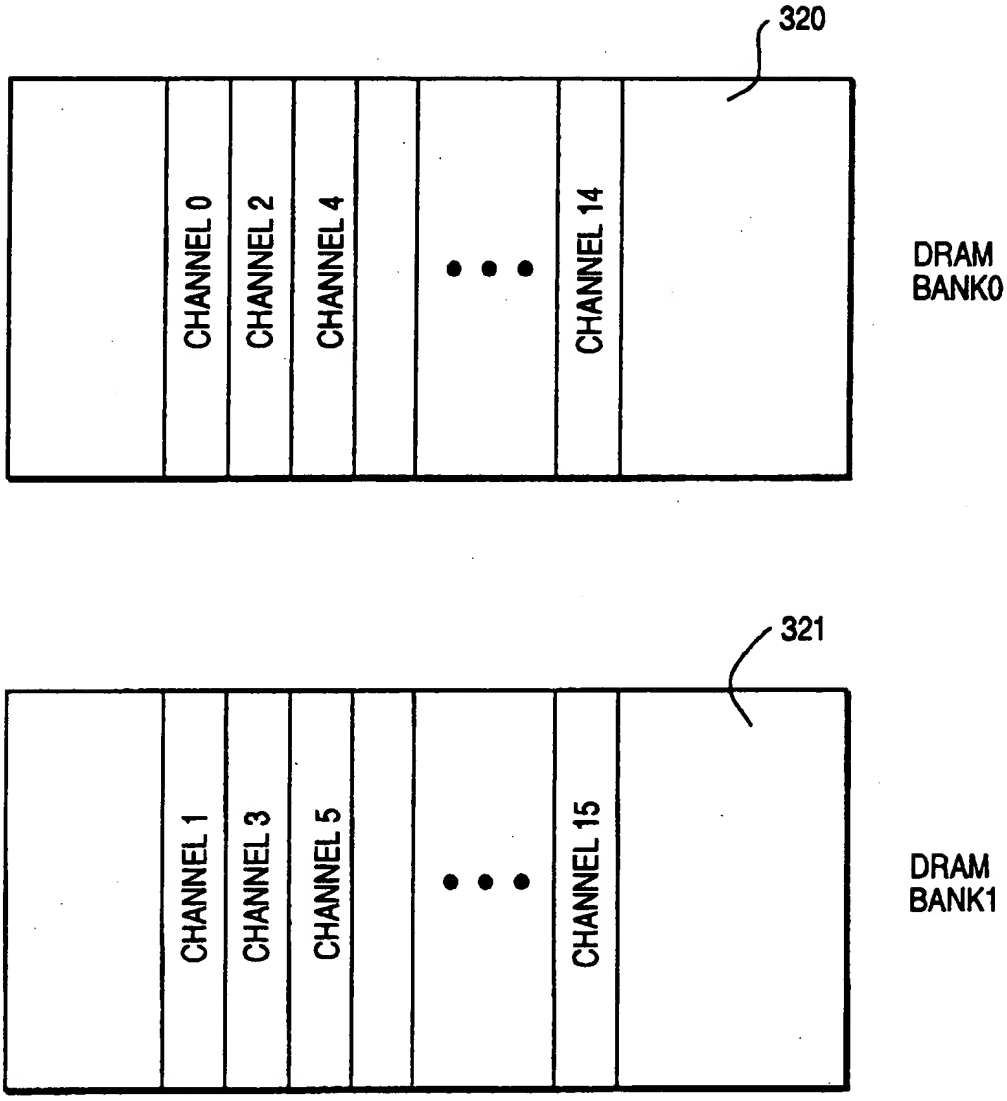

FIG_5

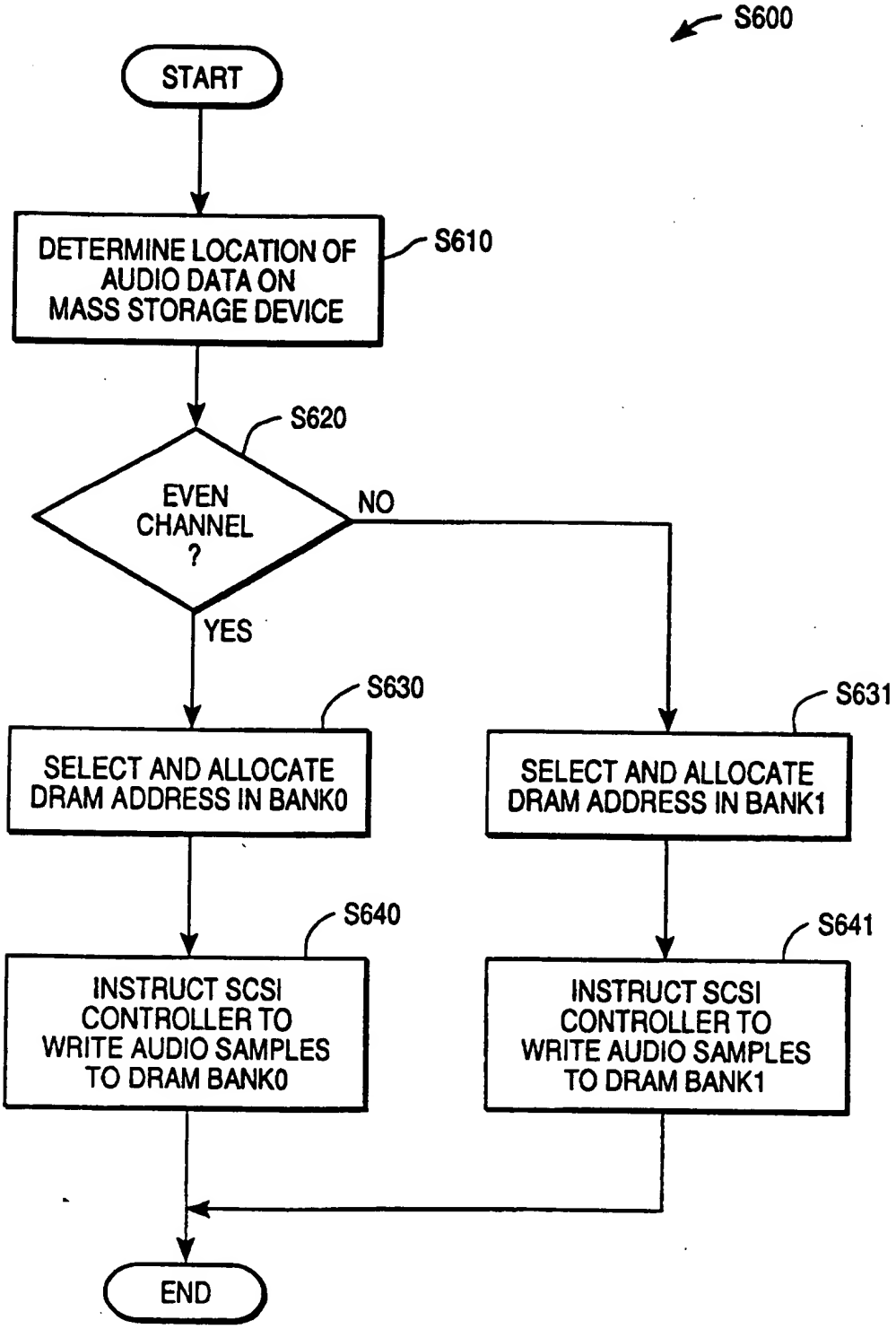
S600
START
DETERMINE LOCATION OF AUDIO DATA ON MASS STORAGE DEVICE
S610
S620
EVEN CHANNEL ?
NO
YES
S630
SELECT AND ALLOCATE DRAM ADDRESS IN BANK0
S631
SELECT AND ALLOCATE DRAM ADDRESS IN BANK1
S640
INSTRUCT SCSI CONTROLLER TO WRITE AUDIO SAMPLES TO DRAM BANK0
S641
INSTRUCT SCSI CONTROLLER TO WRITE AUDIO SAMPLES TO DRAM BANK1
END

FIG_6